

Figure 5 transceiver 170

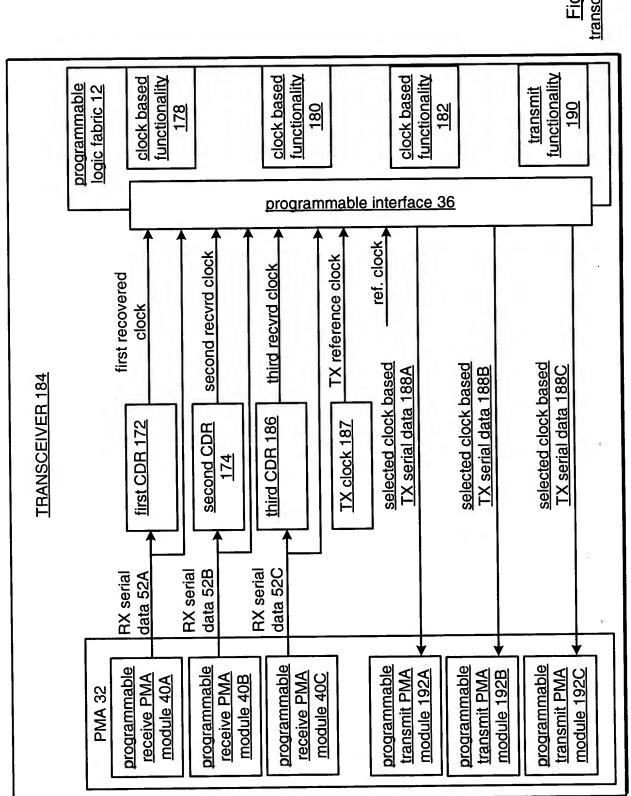


Figure 6 transceiver 184

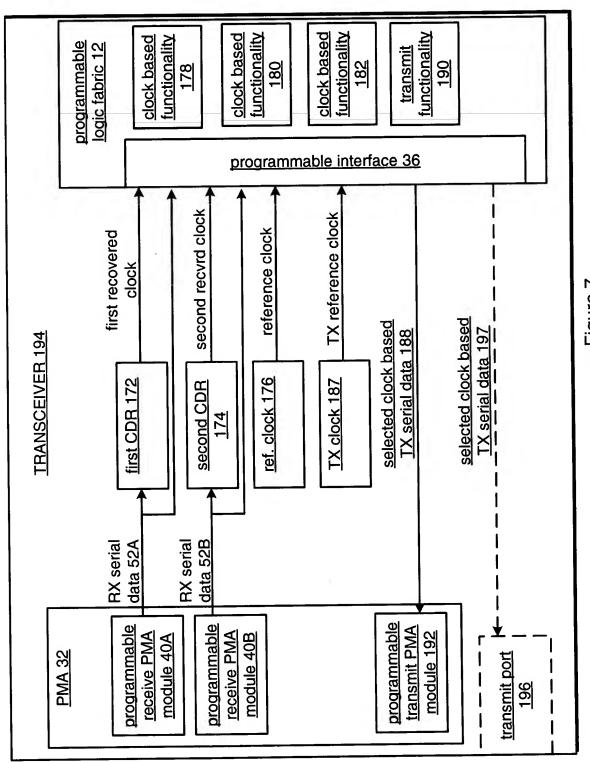


Figure 7 transceiver 194

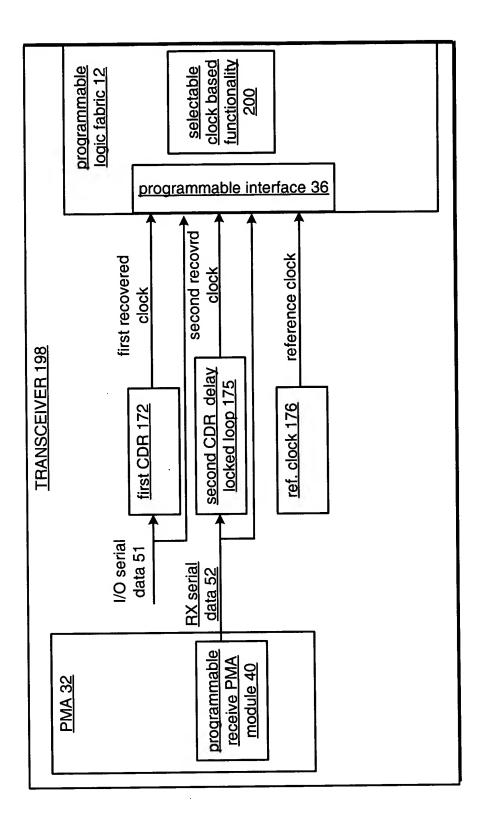
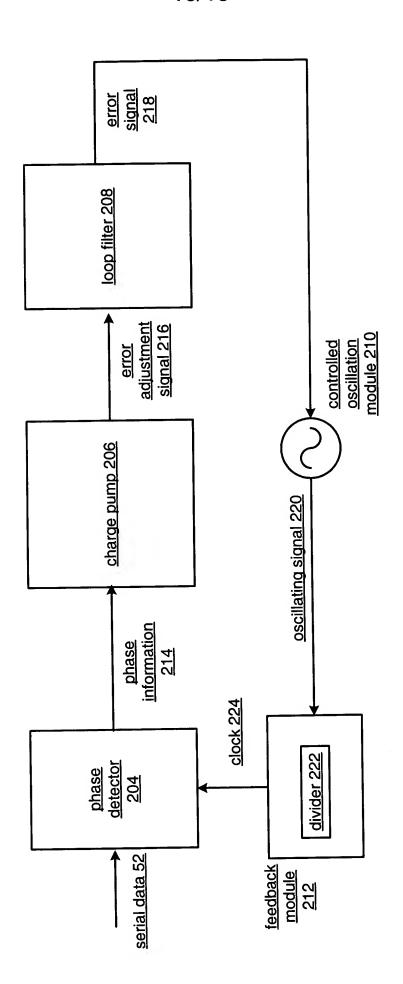
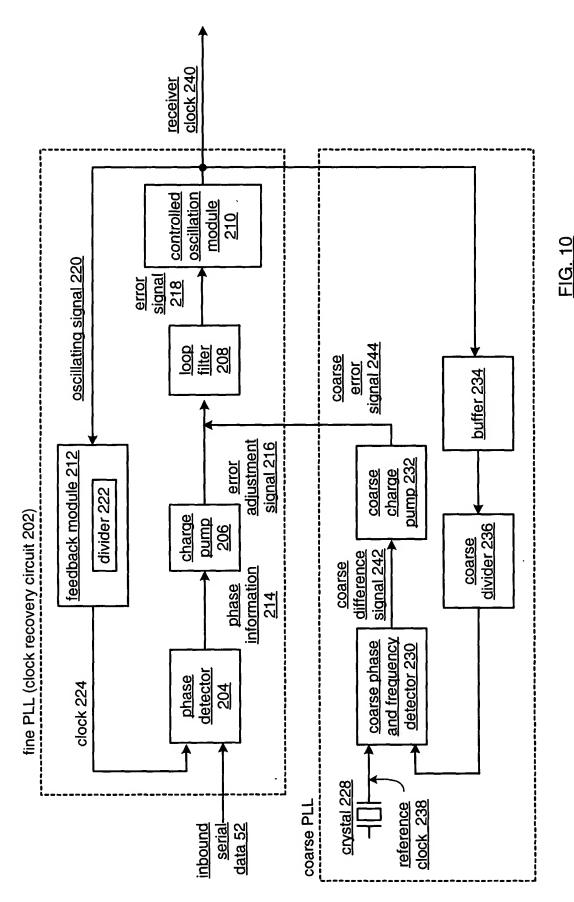


Figure 8 transceiver 198



clock recovery circuit 202



clock data recovery block 226

